

WOMEN'S POLYTECHNIC COLLEGE
(The Pondicherry Institute of Post Matric Technical Education)
(Sponsored by the Government of Puducherry)
LAWSPET, PUDHUCHERRY - 605008.
Telephone: 0413 - 2252833
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No.WPTC/ECE/MODROBS/2012/614

Date: 16/7/2012

To

Authorized Dealers / Suppliers

Sir,

**Sub: WPTC, Puducherry – Purchase of VLSI and Embedded System
Trainer Kits – VLSI & ES Lab - Quotation called for – Reg.**

**Kindly quote / offer your lowest rate for the Purchase of VLSI Trainer Kits –
10nos and Embedded Systems Trainer Kits – 10nos for the use of ECE Department
in this Institution**

The quotation should reach this office on or before 31/07/2012 by 3.00 P.M.

**The quotation should be in sealed cover addressed in the name of PRINCIPAL,
WOMEN'S POLYTECHNIC COLLEGE, Puducherry superscribed as quotation for the
following items mentioned in the Annexure – I & Annexure – II (Enclosed).**

Yours faithfully,

Sd/-
(T.A.BALARAJUSWAMY)
Principal
Women's polytechnic college
Lawspet, Puducherry-605 008.

WOMEN'S POLYTECHNIC COLLEGE
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Ph. No. 0413-2252833.

QUOTATION NOTICE

Sealed quotations are invited by the undersigned for the supply of VLSI trainer kits and embedded systems trainer kits- each 10 Nos. from the authorized dealers /suppliers up to 3.00 p.m. on or before 31.07.2012. The quotation should be supported by the manufacturer's pamphlets/catalogues wherever applicable. The name of the Trainer kits and its specification are enclosed herewith. (Annexure - I, Annexure - II).

Terms and conditions:

1. Prices should be quoted on F.O.R. Pondicherry basis.
2. Taxes such as Excise Duty, Sales Tax, VAT and Central Sales Tax if any should be clearly specified in the quotation D form will be issued by this Institution.
3. E.M.D. of 2.5% on the quoted value by DD favouring PRINCIPAL, WOMEN'S POLYTECHNIC COLLEGE, PUDUCHERRY – 605008 has to be enclosed with your quotation. Quotations without EMD will be summarily rejected.
4. Prices should be inclusive of packing, forwarding and freight charges.
5. Prices quoted must be inclusive of Installation/demonstration/testing charges if any.
6. The delivery period should be written 15 days from the receipt of supply order. It may be extended in the discretion of the undersigned.
7. The materials should be of good and durable quality.
8. Payments will be made only after the receipt of the goods in good condition and to the full satisfaction of the institution.
9. The Articles/Equipments/Machineries/Components/Consumables etc. should have a guarantee period for satisfactory performance/working condition for a period of 12 months from the date of supply. Any defect reported during this period shall be rectified free of charge to the satisfaction of the institution.
10. The rates quoted should be valid for a period of 6 months from the date of quotation.

11. No advance payment will be made and dispatch documents cannot be accepted through bank.
12. The undersigned reserves the right to reject quotation not conforming to the specifications of the items and terms and conditions. Any further details can be had from the office during the office hours.
13. The details mentioned above have been uploaded in the DHTE, Puducherry website.

<http://dhte.puducherry.gov.in>

Sd/-

Principal

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lawspet, Puducherry-605 008**

ANNEXURE – I

Detailed Specifications for VLSI XILINX SPARTAN 3E FPGA TRAINER KIT - 10 Nos.

SPECIFICATION :

- Xilinx XC3S250E- PQ208 Spartan 3E FPGA
- 250K gates, 5508 Logic cells, 216K Bits of block Ram & 12 dedicated multipliers.
- 16 Nos. of digital input using switches with LED indication
- 16 Nos. of digital outputs using discrete LEDs
- 16 x 2 LCD is provided for display the text message
- One reset switch
- One switch for manual clock
- FPGA configuration through
 - JTAG port
 - On board Flash PROM XCF02S
- Parallel downloading cable of length 2m
- Inbuilt power supply +5V
- Total 158 I/O pins: 124 pins used for integrating peripheral LED, switches etc., balances pins available to user in 20 pin header (3.3V compatible)
- On board programmable PLL oscillator from 3 MHz to 100 MHz using jumpers
- 4 Nos. of 7 segment LED display
- Compatible with Xilinx ISE Foundation / Web pack software
- On board USB to JTAG Port
- Supporting software for Windows 7 Operating System.

ANNEXURE - II

Detailed Specifications for Embedded Systems ARM7 Trainer kit - 10 Nos.

On- Chip Features:

- 16/32 – bit ARM7 TDMI-S Core at 60 MHz
- 40 KB of on-chip static RAM and 512 KB of on-chip flash program memory.
- In-System/In-Application Programming (ISP/IAP) via on-chip boot-loader software.
- USB 2.0 Full Speed compliant Device controller
- Two 10 bit ADC capable of providing 14 analog input channels
- Single 10-bit D/A converter provide variable analog output.
- Two 32 bit timers/external event counters (with four capture and four compare channels each), PWM Unit (six outputs) and watchdog.
- Multiple serial interface including two UART, two Fast I2C- bus (400 Kbit/s), SPI and SSP with buffering and variable data length capabilities.
- Upto 45 of 5 V tolerant fast general purpose I/O pins

Off- Chip Features:

- 8 Bit Digital output ---LED Interface
- 8 Bit Digital Input ---Switch Interface
- 16x 2 Alpha numeric character LCD
- ADC Trimmer
- RTC Interface
- Seven Segment display interface
- Temperature sensor
- One UART Port
- PWM Signals are terminated at RMC Header
- GPIO Lines are terminated at Expansion Header
- Built-in Power Supply
- USB to Serial Converter
- USB to JTAG port.
- Supporting software for Windows 7 Operating System.